

FIG. 1

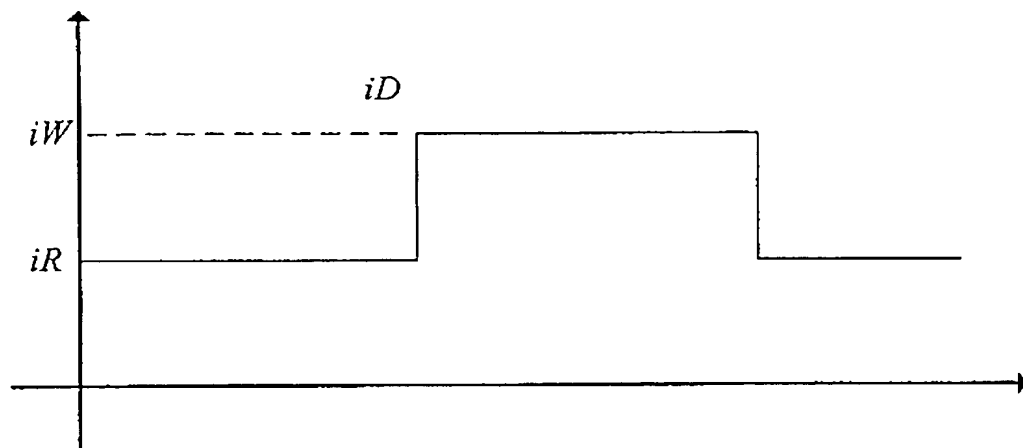


FIG. 2

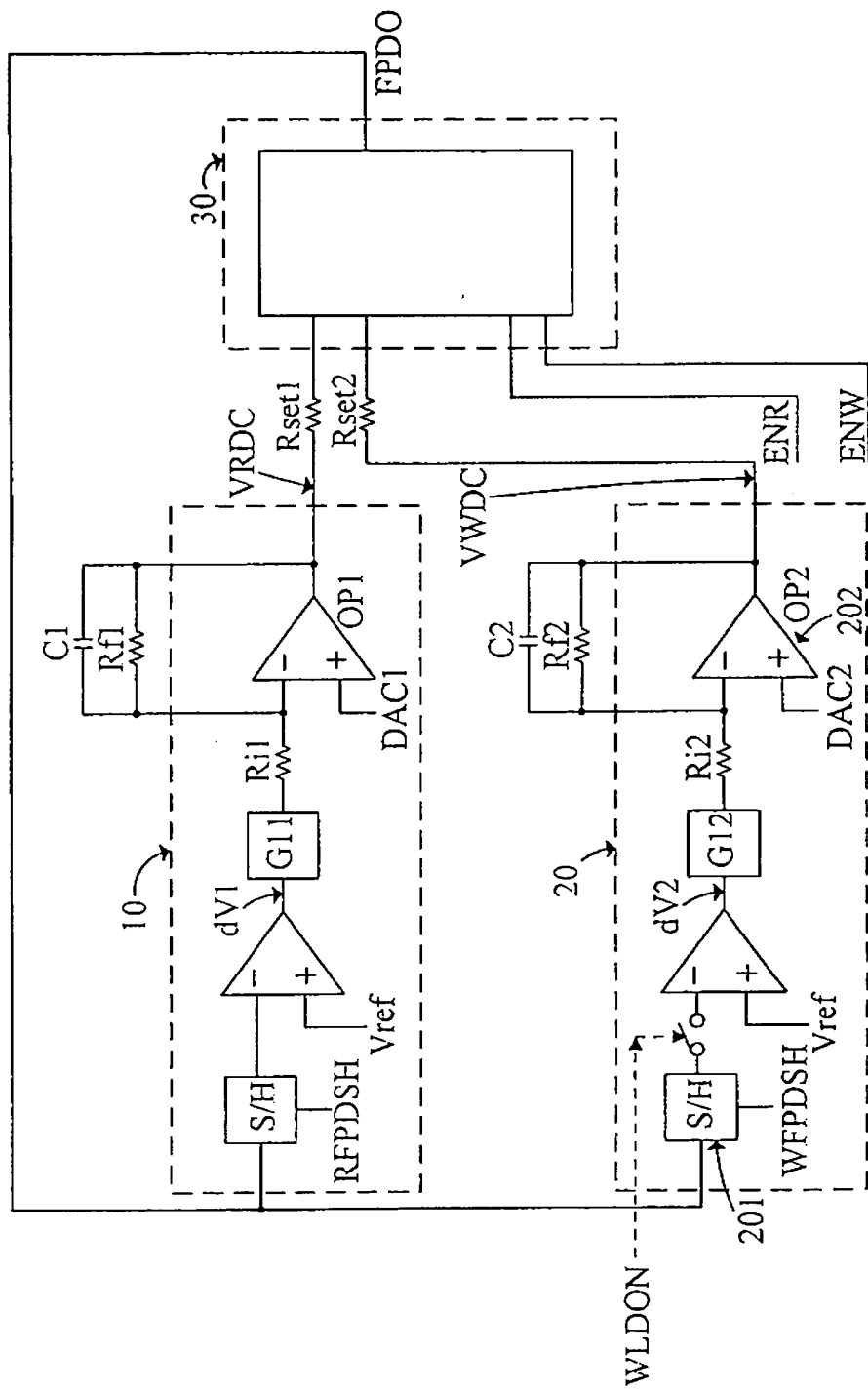


FIG. 3

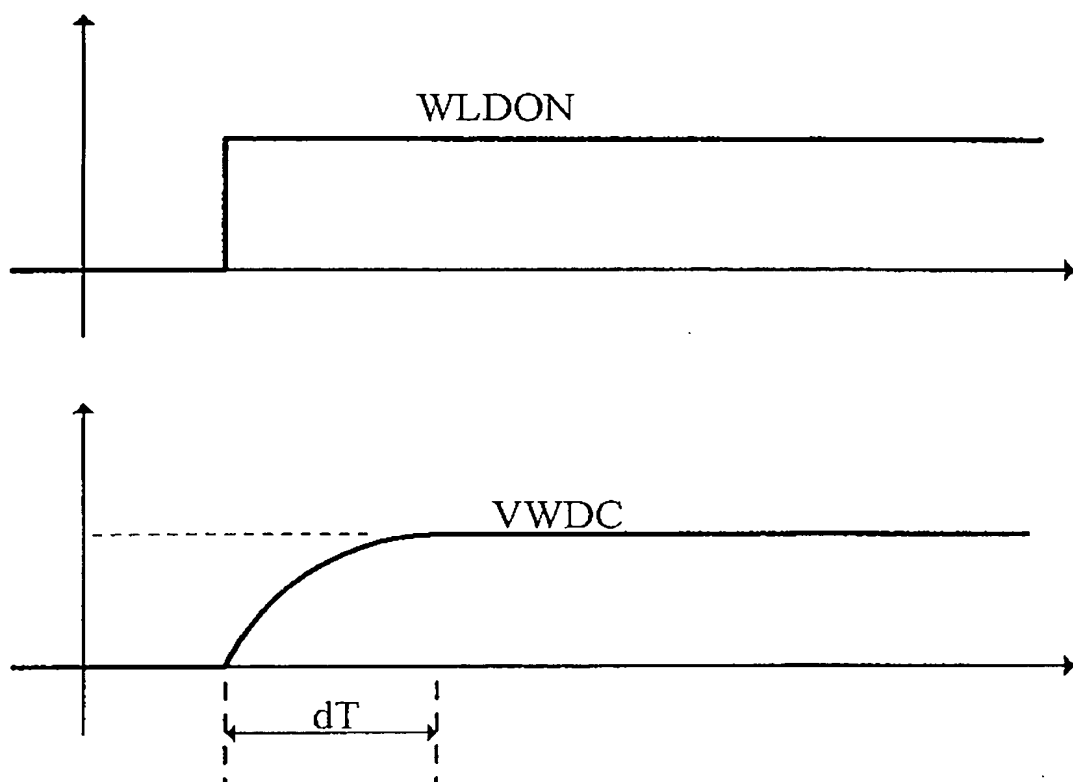
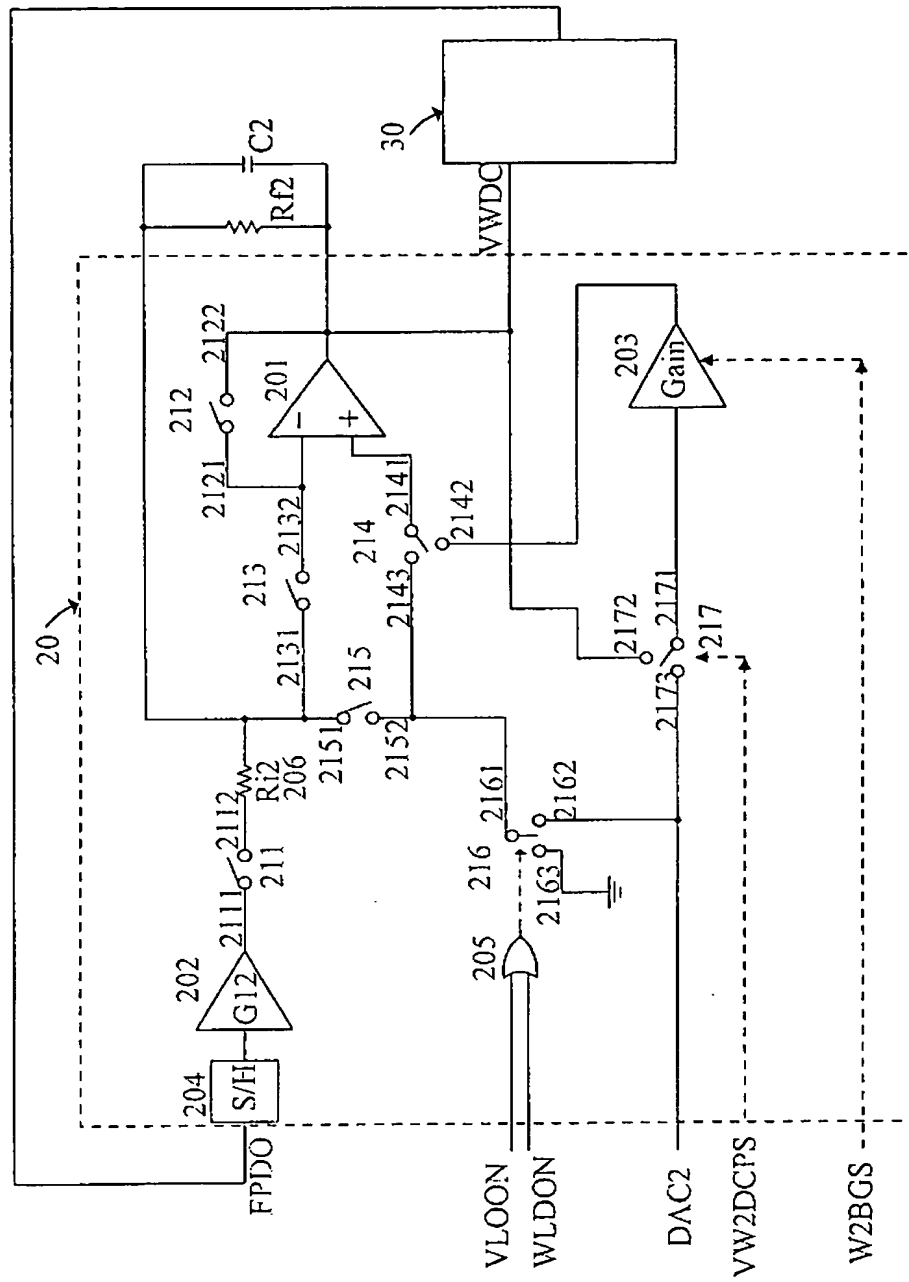


FIG. 4



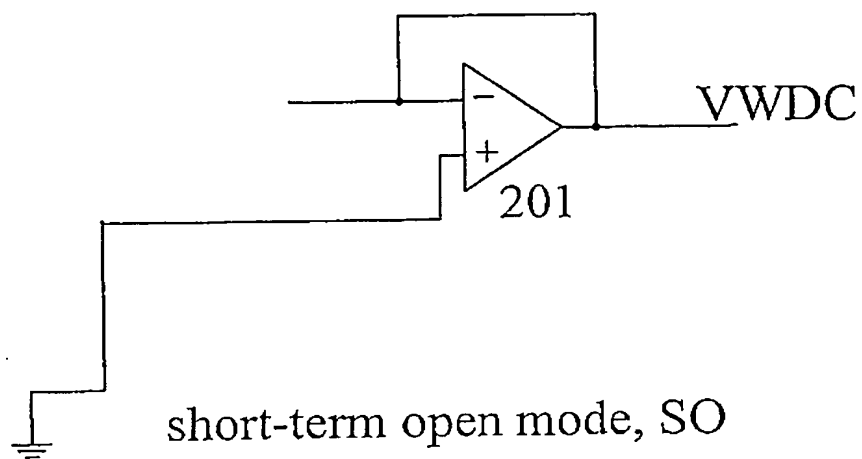


FIG. 6

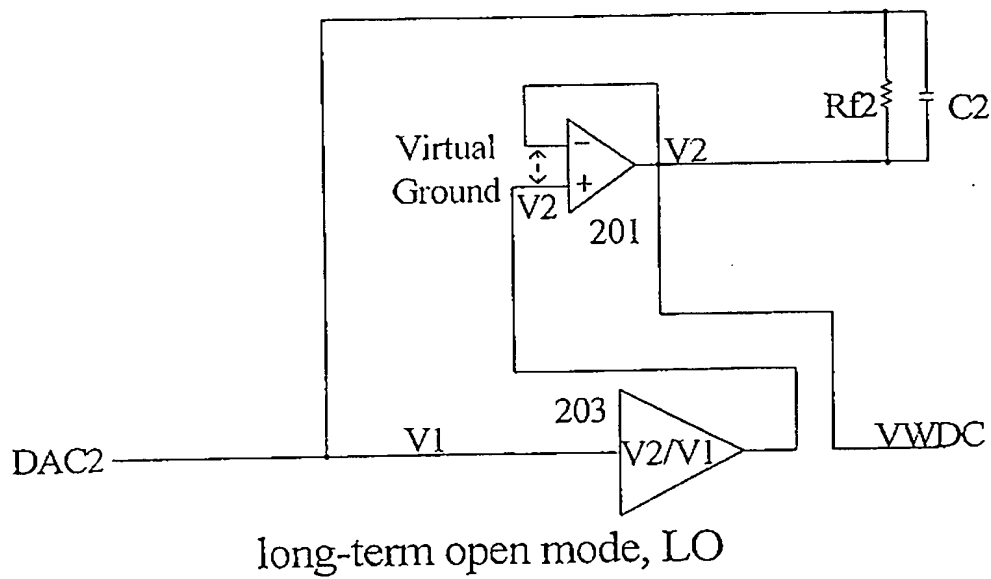
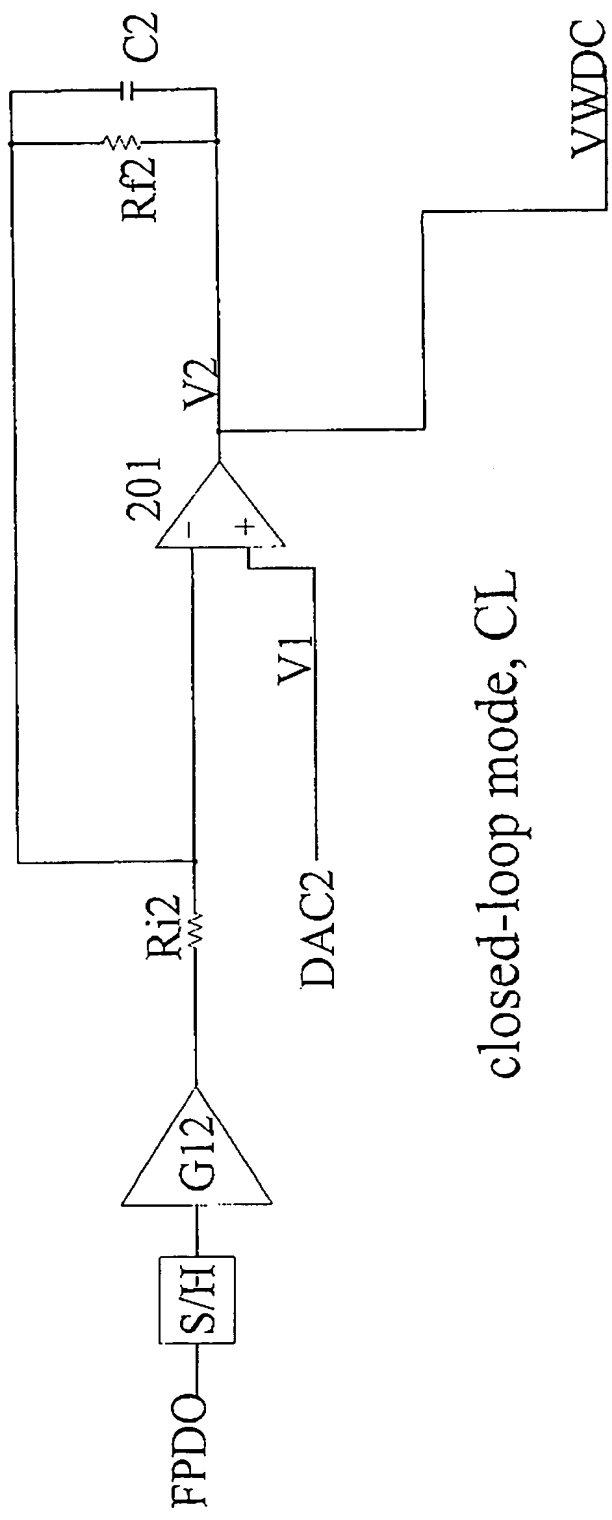


FIG. 7



closed-loop mode, CL

FIG. 8

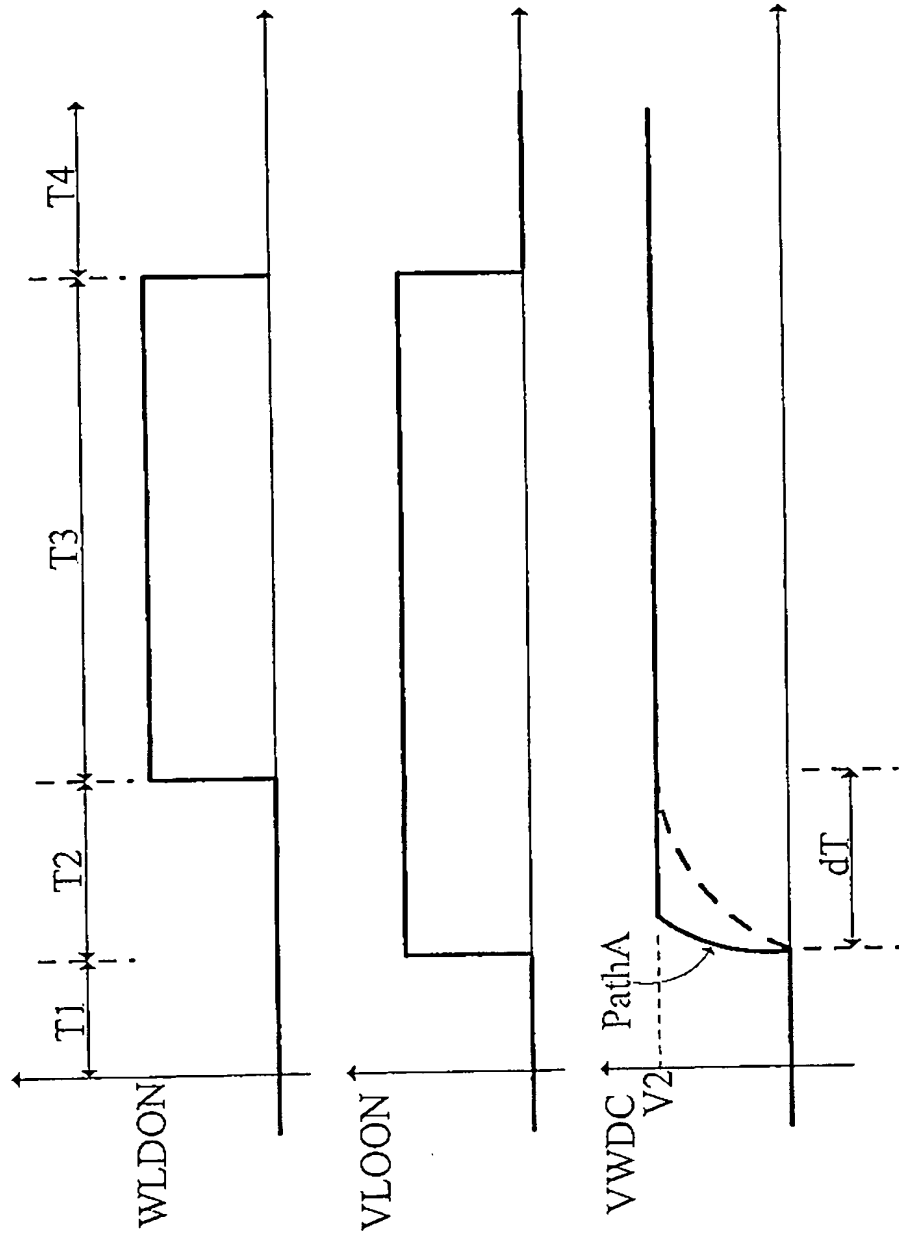


FIG. 9